

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

JUNG-KEUN AHN *et al.*

Serial No.: *to be assigned*

Examiner: *to be assigned*

Filed: 3 November 2003

Art Unit: *to be assigned*

For: PLASMA DISPLAY PANEL

**INFORMATION DISCLOSURE STATEMENT**

**Mail Stop Patent Application**

Commissioner for Patents

P.O.Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites and provides copies of the following art references:

1. Japanese Patent Publication No. 2001-160360 to Saito *et al.*, entitled *PLASMA DISPLAY PANEL*, published on 12 June 2001 which pertains to a plasma display panel which suppresses abnormal discharges caused by dummy ribs and improves the strength of dummy ribs (English language Abstract is attached);
2. U.S. Patent Publication No. 2002/0050788 published on May 2, 2002, to Kimura *et al.*, entitled *PLASMA DISPLAY APPARATUS HAVING REINFORCED ELECTRONIC CIRCUIT MODULE*, published on May 2, 2002;
3. U.S. Patent No. 6,166,711 to Odake, entitled *PLASMA ADDRESSED ELECTRO-OPTICAL DISPLAY*, issued on December 26, 2000;
4. U.S. Patent No. 6,400,079 to Baret *et al.*, entitled *PLASMA PANEL WITH CELL CONDITIONING EFFECT*, issued on June 4, 2002;

5. U.S. Patent No. 6,285,128 to Amemiya, entitled *SURFACE DISCHARGE TYPE PLASMA DISPLAY PANEL*, issued on September 4, 2001;
6. U.S. Patent No. 5,684,362 to Togawa, entitled *PLASMA ADDRESSED ELECTRO-OPTICAL DEVICE HAVING A PLASMA DISCHARGE CHAMBER*, issued on November 4, 1997;
7. U.S. Patent No. 6,400,080 to Tanaka, entitled *PLASMA DISPLAY PANEL HAVING A SEALING LAYER AND FIRST, SECOND, AND THIRD WALLS*, issued on June 4, 2002;
8. U.S. Patent No. 5,886,467 to Kimura, entitled *PLASMA ADDRESSED LIQUID CRYSTAL DISPLAY DEVICE*, issued on March 23, 1999; and
9. U.S. Patent No. 6,590,332 to Park *et al.*, entitled *PLASMA DISPLAY PANEL INCLUDING FRONT AND REAR SUBSTRATE ASSEMBLIES*, issued on July 8, 2003.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

No fee is incurred by this Statement.

Respectfully submitted,

  
\_\_\_\_\_  
Robert E. Bushnell  
Reg. No.: 27,774

1522 "K" Street, N.W., Suite 300  
Washington, D.C. 20005  
Area Code: (202) 408-9040

Folio: P56975  
Date: 3 November 2003  
I.D.: REB/kf

<b>INFORMATION DISCLOSURE STATEMENT</b> <b>PTO-1449 (PAGE 1 OF1)</b>	SERIAL NUMBER	DOCKET NO.
	APPLICANT	JUNG-KEUN AHN
	FILING DATE 3 November 2003	GROUP

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

## **TRANSLATION**

FOREIGN PATENT DOCUMENTS						MANUFACTURER	
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	JP2001-160360	6/01	Japan				

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)**


**EXAMINER:** \_\_\_\_\_ **DATE CONSIDERED:** \_\_\_\_\_

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.